

PATENT NUMBER

U.S. UTILITY Patent Application

<p>O.I.P.E.</p> <p>SCANNED <u>PTD²</u> O.A. <u>A.G.</u></p>	<p>PATENT DATE</p>
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CLASSIFICATION NO. 09/620649	CONT/PRIOR D	CLASS 438 257	SUBCLASS 316	ART UNIT 2822	EXAMINER W/ozcwsR/
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APPLICANTS

Cetin Kaya

3.1.1.1

Integrated circuit having independently formed array and peripheral isolation dielectrics

PTO-2040
1259

ISSUING CLASSIFICATION								
ORIGINAL		CROSS REFERENCE(S)						
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
257	316	438	257					
INTERNATIONAL CLASSIFICATION								
A01L	29/788							

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED		
	Sheets Drwg. <i>5</i>	Figs. Drwg. <i>10</i>	Print Fig./ <i>4d</i>	Total Claims <i>1</i>	Print Claim for O.G. <i>1</i>	
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED <i>02/11/04</i>		
	<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____			ISSUE FEE <table border="1"> <tr> <td>Amount Due <i>\$1330</i></td> <td>Date Paid</td> </tr> </table>		Amount Due <i>\$1330</i>
Amount Due <i>\$1330</i>	Date Paid					
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	<i>Muller</i> <i>2/2004</i> (Primary Examiner) (Date)			ISSUE BATCH NUMBER _____ (Legal Instruments Examiner) (Date)		
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